

REMARKS

Drawing submittal.

Attached please find six (6) sheets of drawings in the above-identified application.

Please charge any other fees which might be due with respect to Sections 1.16 and 1.17 to the Deposit Account of Lerner and Greenberg, P.A., No. 12-1099.

Respectfully submitted,



Loren Donald Pearson (Reg. No. 42,987)

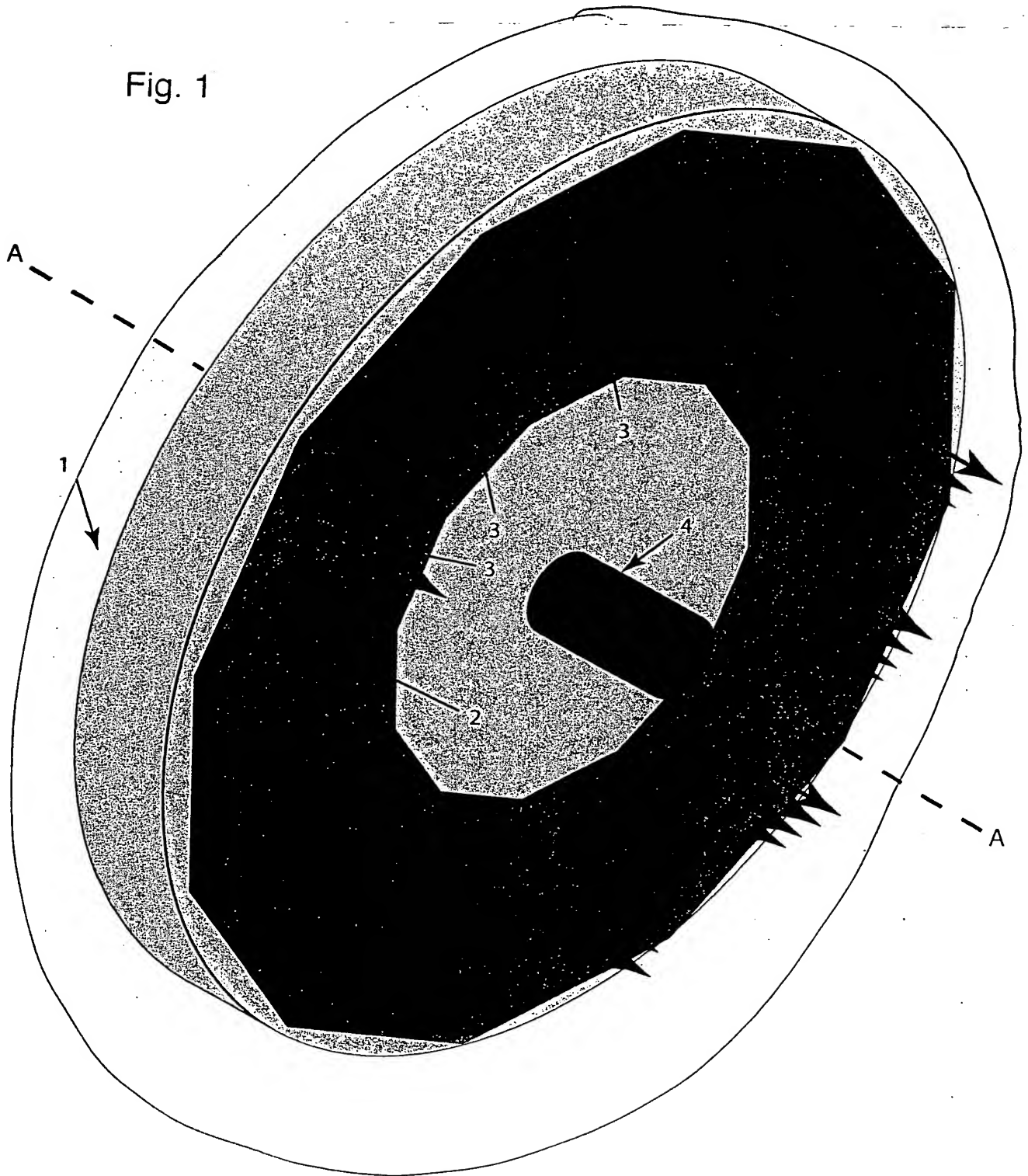
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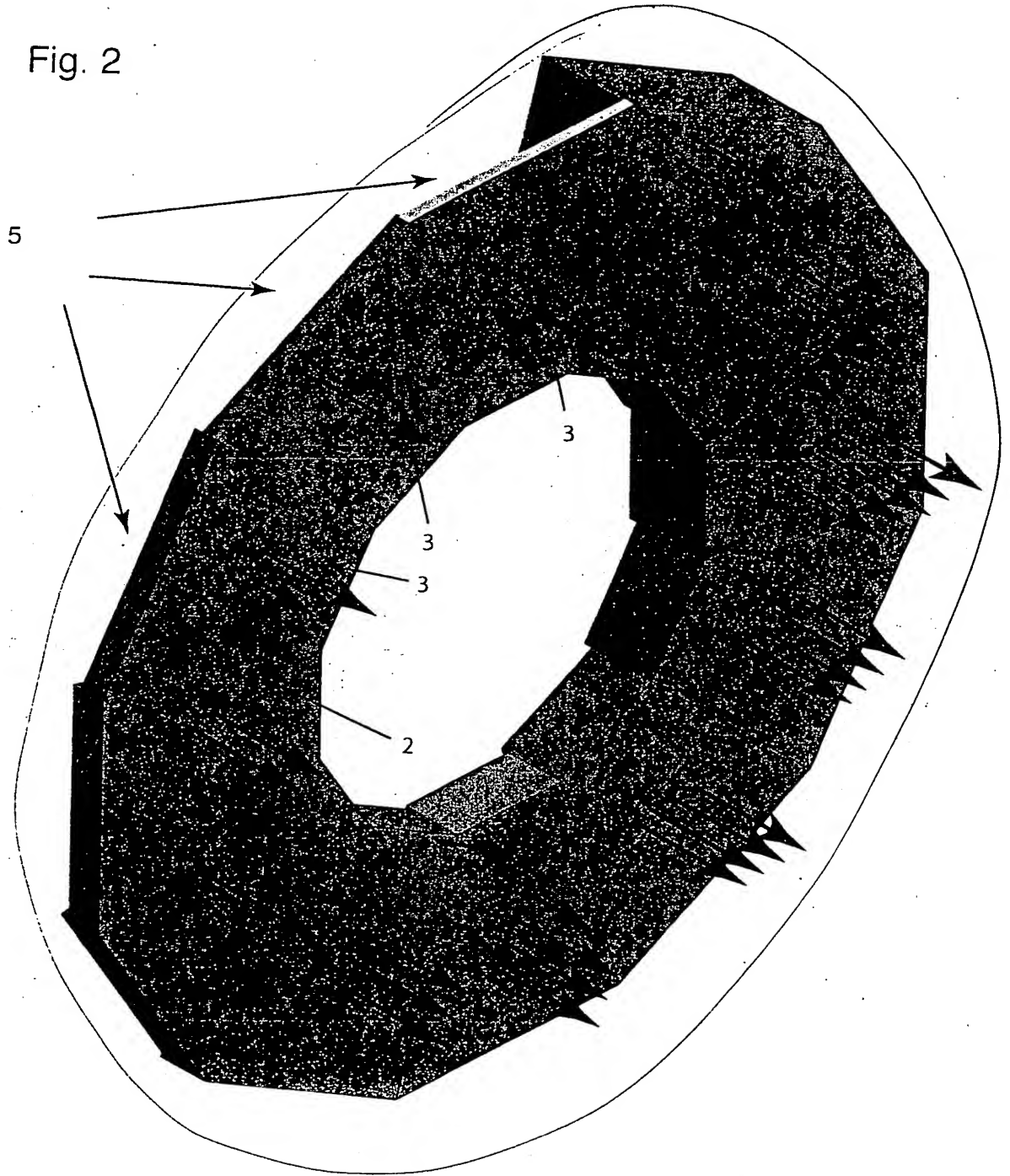
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Fig. 1



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Fig. 2



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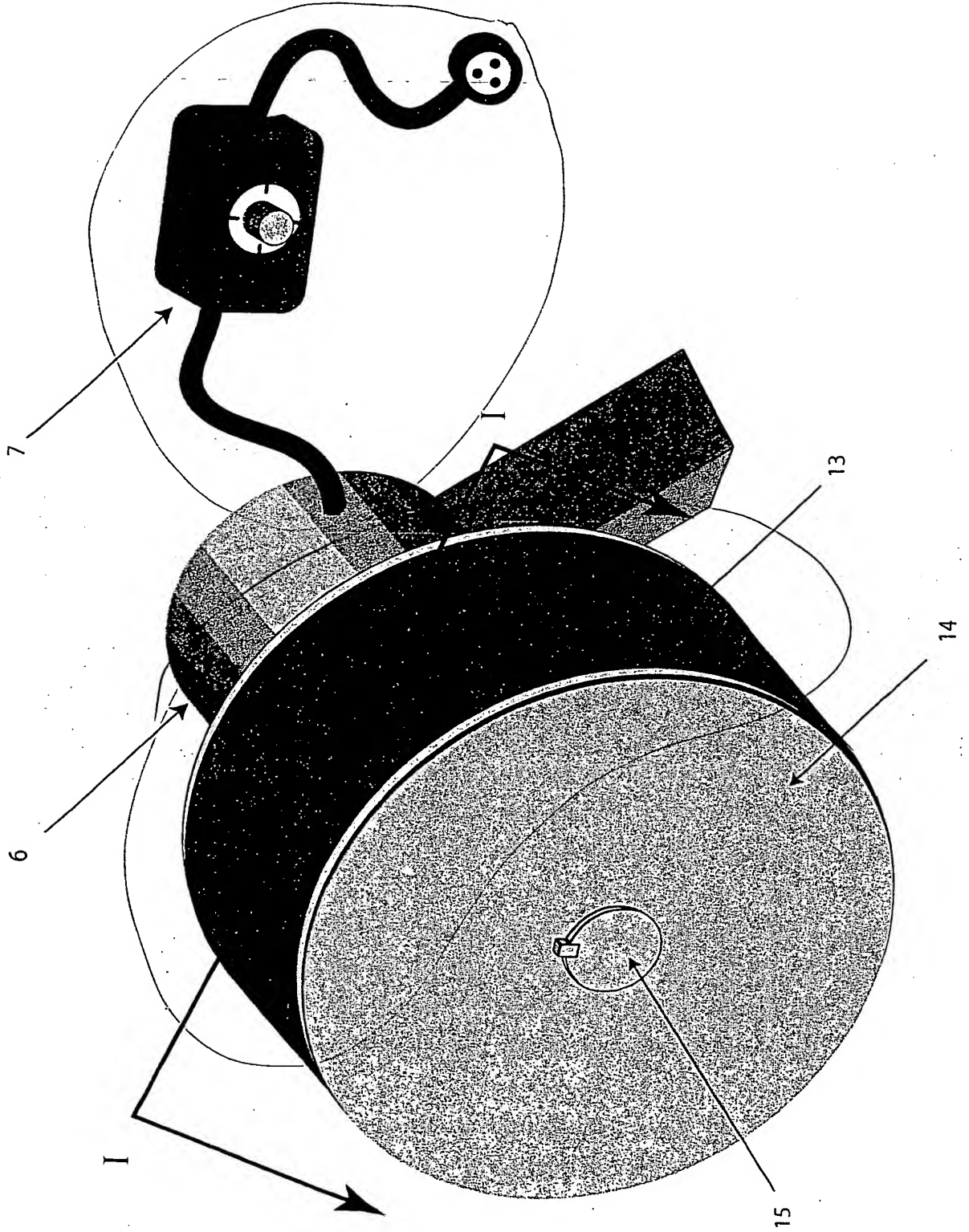


Fig. 4

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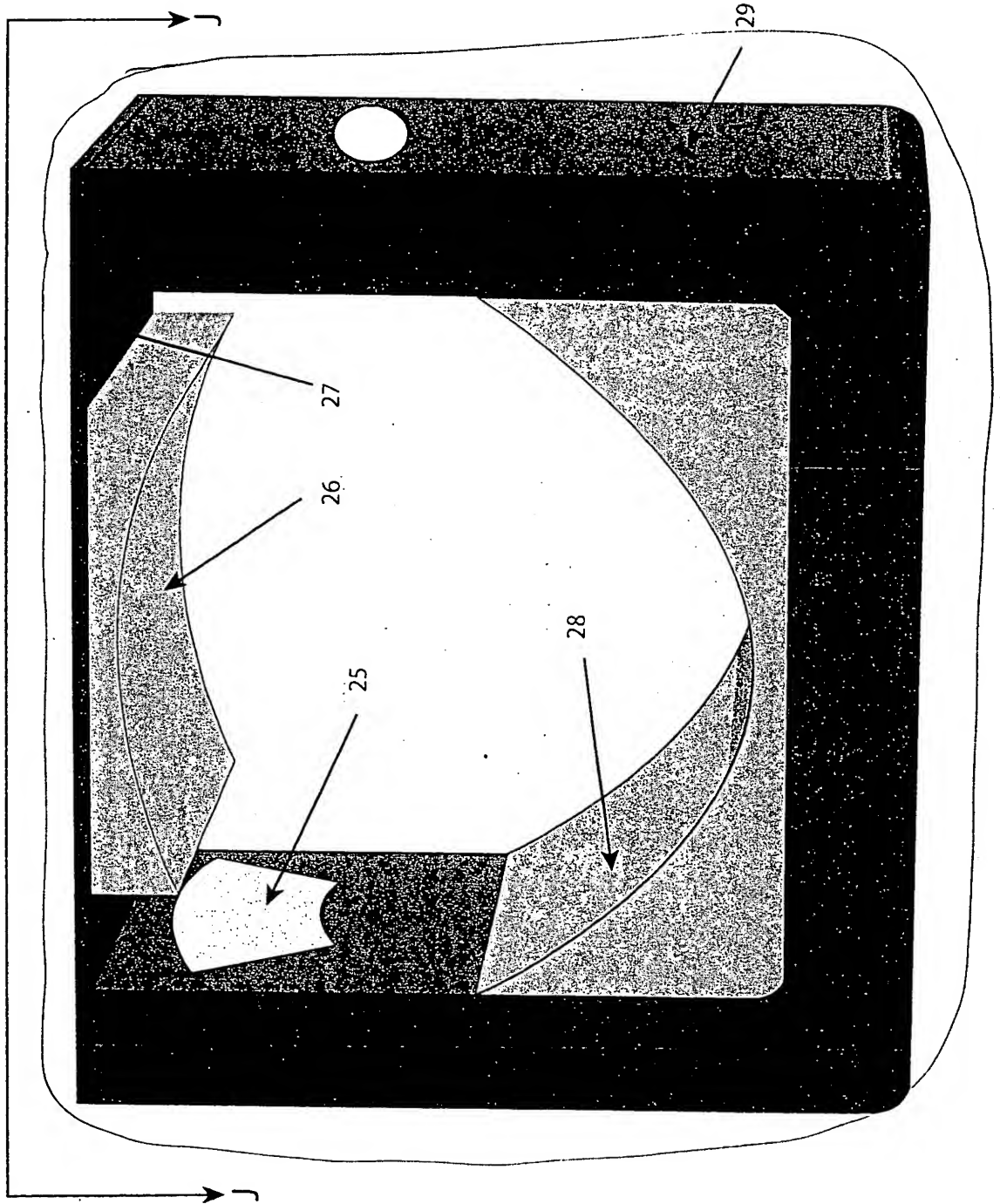


Fig. 9

The diagram shows a power coil system. A power supply (106) is connected to a resistor (8). The other end of resistor (8) is connected to the gate of an IGBT (110). The IGBT (110) is driven by a driver circuit (109) which includes a pulse generator, a driver chip, and a driver coil. The IGBT (110) is connected to a power coil (102) which consists of an inductor (L) and a resistor (R_L). The power coil (102) is connected to a capacitor (13) and a diode (14). The diode (14) is connected to the IGBT (110). The power coil (102) is also connected to a resistor (R_P) which is connected to the power supply (106). The power coil (102) is labeled as being externally mounted.

Labels:

- L is the inductance of the Power Coil
- R_L is the resistance of the Power Coil
- R_P is the high resistance of the decay circuit

L is the inductance of the Power Coil
 R_L is the resistance of the Power Coil
 R_r is the high resistance of the decay circuit
 D is the blocking diode preventing reverse current flow
IGBT is a high speed gate that opens and closes the circuit

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FIG. 13A

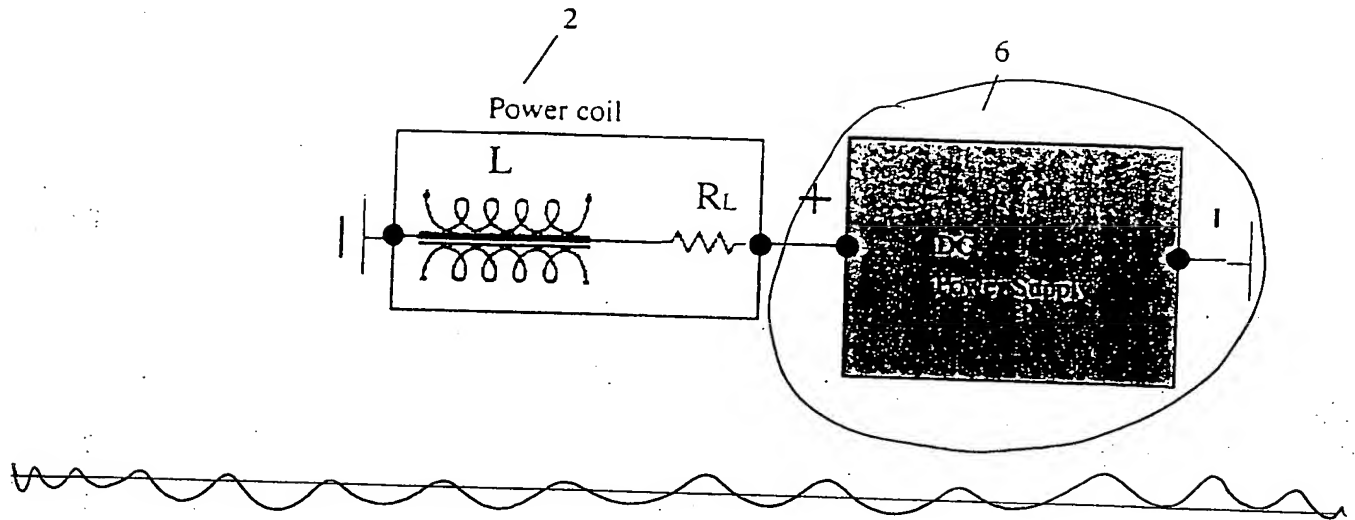


FIG. 13B

